

1. Answer : B. Only A and B will meet this clock rate and integration level but a full custom design would take way too long and would be overkill/.
2. C. $1 + 1 + 6 + 3*2 = 14$ ns
3. Answer: C $T_{hold} + T_{skew} = 3$ ns, fastest $T_{logic} = 3$ ns
4. Answer : C, both of these are correct.
5. Answer : B $10 - 3 - 3 = 4$ ns
6. Answer : C
7. Answer: D
8. Answer : D
9. Answer : A
10. Answer : D.
11. Answer : B.
12. Answer : B