

## Analog Alliance

### Wired Communications Circuit Design

Example outcomes gained by taking this track would include joining groups that design and maintain interface chips and cards for Ethernet, Telephone systems, ADSL, Backplane transceivers, and Cable Modems. Local employers looking for skills in this area would include **Analog Devices, IBM, Nortel, Maxim, Cadence, Velio, and Cisco.**

Courses central to obtaining this outcome are the following:

- ECE 546 VLSI Design
- ECE 511 Analog Electronics
- ECE 515 Digital Communications
- ECE 513 Digital Signal Processing
- ECE 712 Analog VLSI
- ECE 733 Digital Circuits

Additional courses you should consider include the following:

- ECE 520 Digital ASIC Design

### Sample MS Plans of Work

These plans of work are suggestions only. Feel free to seek other advice or to structure your own curricula. Please note that the actual courses taught are constantly changing, and these might be out of date. These are written assuming you are a full time student taking 10 courses over three semesters. You might choose to not overload in Spring, but take one last course in Spring after this.

Full Custom High Speed Signals for Microprocessors, Transceivers, and Backplanes					
Fall		Spring		Fall	
ECE 546	D	ECE 544	A/RF/D	ECE 733	D
ECE 511	A	ECE 520	D	ECE 515	COM
ECE 521	D	ECE 712	A		
		ECE 756	D		
		ECE 513	DSP		

### Associated Faculty

Dr. Paul Franzon

Last edited by K. Gard 9/21/2006